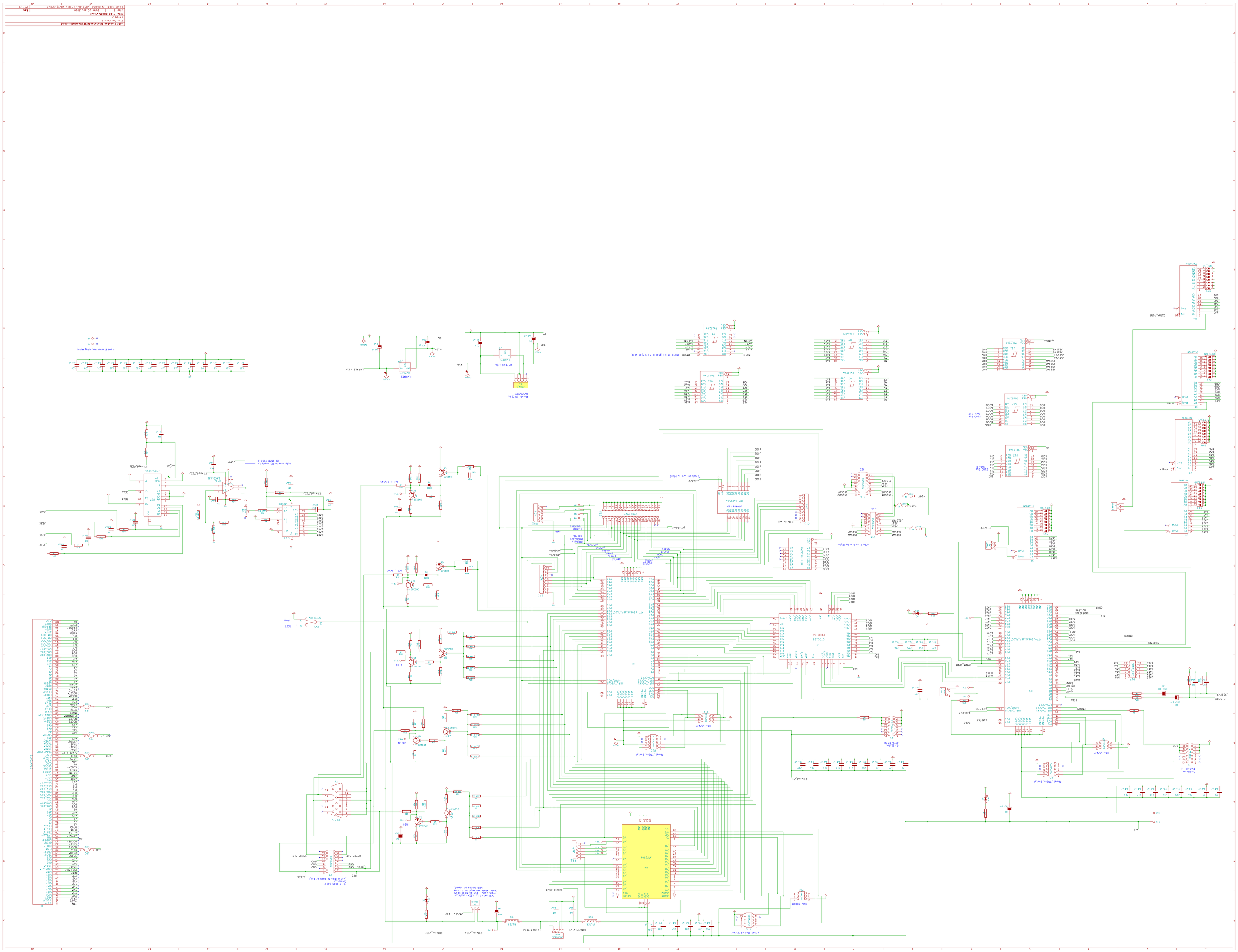
Adm Mojahdm (mojahdm@SSDComputers.com)
Fmr Dsgnrs:n/a
Sheets /
Title: SSDD 80486 V1.4A
Size: E | Date: 15 Aug 2016
KiCad E.D.A. eeschema [2015-07-07 BZR 5723]-release
Id: 1/1
Switch
Reset
Clock on Low to high)
Clock on Low to high)
Note wire to U1 needs to be input bias 3-
Wire wrapped to Pin 1 of U1 (Note: Pin 1 is the pin closest to the edge of the board)